

IN THE SPECIFICATION:

2<sup>nd</sup> full paragraph on page 1

In this type of circuit that has heretofore been used, a high voltage is applied to a terminal for inputting a signal such as an address to thereby assert a test circuit select signal so as to select a test circuit. ~~Fig. 7 shows a typical diagram of a conventional test circuit selecting method.~~ An input terminal A corresponds to a suitable input terminal of a memory chip. Unless otherwise stated below in the present Specification, "L" indicates a ground level, and "H" indicates a power supply voltage level, respectively. Further, "HV" indicates a high voltage level for selecting a test circuit.